



Sheet 1 of 1

U.S. Department of Commerce, Patent and Trademark Office					Atty Docket No.		Serial No.	
					M-15204-1D US		10/803,599	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT					Applicant			
(Use several sheets if necessary)					Yi Ding			
					Filing Date		Group	
					March 17, 2004		Unassigned	
U.S. Patent Documents								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
PD	AA	6,420,231	16 Jul. 2002	Harari et al.	—	—		
PD	AB	2003/0218908 A1	27 Nov. 2003	Park et al.	—	—		
PD	AC	2004/0004863 A1	8 Jan. 2004	Wang	—	—		
	AD							
	AE							
	AF							
	AG							
	AH							
Foreign Patent Documents								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
PP	AI	EP 0 938 098 A2	25 Aug. 1999	Europe	—	—		
	AJ							
	AK							
	AL							
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
PD	AM	United States Patent Application No. 10/798,475, entitled "Fabrication of Conductive Lines Interconnecting Conductive Gates in Nonvolatile Memories and Non-Volatile Memory Structures," Filed on March 10, 2004; Attorney Docket No. M-15296 US.						
PD	AN	United States Patent Application No. 10/797,972, entitled "Fabrication of Conductive Lines Interconnecting First Conductive Gates in Nonvolatile Memories Having Second Conductive Gates Provided By Conductive Gates Lines, Wherein The Adjacent Conductive Gate Lines For The Adjacent Columns Are Spaced From Each Other, And Non-Volatile Memory Structures," Filed on March 10, 2004; Attorney Docket No. M-15297 US.						
	AO							
	AP							
Examiner		PHUC T. DANG		Date Considered		9/29/2004		
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.								